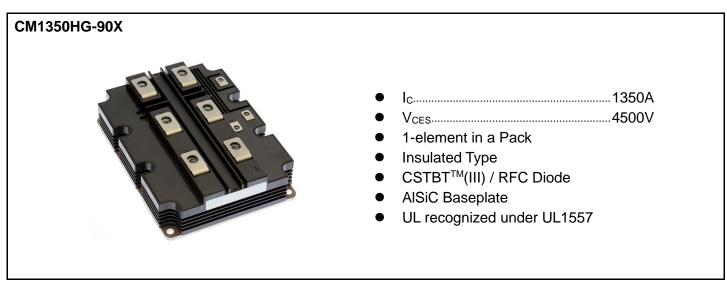


< High Voltage Insulated Gate Bipolar Transistor: HVIGBT >

## CM1350HG-90X

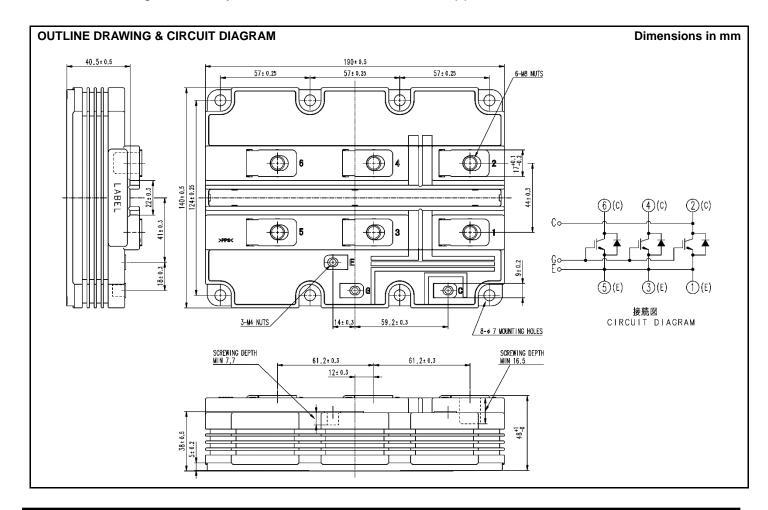
HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules



### **APPLICATION**

Traction drives, High Reliability Converters / Inverters, DC choppers



### < High Voltage Insulated Gate Bipolar Transistor: HVIGBT >

### CM1350HG-90X

HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

#### **MAXIMUM RATINGS**

Symbol	Item	Conditions	Ratings	Unit
V <sub>CES</sub>	Collector-emitter voltage	$V_{GE} = 0V, T_j = -40+150$ °C	4500	V
		$V_{GE} = 0V, T_j = -50^{\circ}C$	4400	٧
$V_{GES}$	Gate-emitter voltage	$V_{CE} = 0V, T_j = 25^{\circ}C$	± 20	V
Ic	Callantar assument	DC, $T_C = 105^{\circ}C$	1350	Α
I <sub>CRM</sub>	Collector current	Pulse (Note 1)	2700	Α
I <sub>E</sub>	Emitter current (Note 2)	DC	1350	Α
I <sub>ERM</sub>	Emitter current (Note 2)	Pulse (Note 1)	2700	Α
P <sub>tot</sub>	Maximum power dissipation (Note 3)	T <sub>C</sub> = 25°C, IGBT part	14700	W
V <sub>iso</sub>	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1 min.	10200	V
V <sub>e</sub>	Partial discharge extinction voltage	RMS, sinusoidal, f = 60Hz, Q <sub>PD</sub> ≤ 10 pC	5100	V
Tj	Junction temperature		<b>−</b> 50 ~ <b>+</b> 150	°C
T <sub>jop</sub>	Operating junction temperature		<b>−</b> 50 ~ <b>+</b> 150	°C
T <sub>stg</sub>	Storage temperature		<b>−</b> 55 ~ <b>+</b> 150	°C
t <sub>psc</sub>	Short circuit pulse width	$V_{CC} = 3400V, V_{CE} \le V_{CES}, V_{GE} = 15V, T_j = 150$ °C	10	μs

#### **ELECTRICAL CHARACTERISTICS**

Cumbal	ltom	Conditions			Limits		Unit
Symbol	mbol Item Conditions			Min	Тур	Max	Unit
			T <sub>j</sub> = 25°C	_	_	10.0	mA
I <sub>CES</sub>	Collector cutoff current	$V_{CE} = V_{CES}, V_{GE} = 0V$	T <sub>j</sub> = 125°C	_	10.0	_	
			T <sub>j</sub> = 150°C	_	60.0	_	
$V_{GE(th)}$	Gate-emitter threshold voltage	$V_{CE} = 10 \text{ V}, I_{C} = 135 \text{ mA}, T_{j} = 25^{\circ}\text{C}$		6.5	7.0	7.5	٧
$I_{GES}$	Gate leakage current	$V_{GE} = V_{GES}$ , $V_{CE} = 0V$ , $T_j = 25$ °C		-0.5	_	0.5	μΑ
Cies	Input capacitance	V 40.V.V 0.V.£ 400.H.I=		_	170	_	nF
C <sub>oes</sub>	Output capacitance	$V_{CE} = 10 \text{ V}, V_{GE} = 0 \text{ V}, f = 100 \text{ kHz}$		_	11.0	_	nF
C <sub>res</sub>	Reverse transfer capacitance	T <sub>j</sub> = 25°C		_	1.5	_	nF
Q <sub>G</sub>	Total gate charge	$V_{CC} = 2800V$ , $I_C = 1350A$ , $V_{GE} = \pm 15V$		_	12.6	_	μC
		I <sub>C</sub> = 1350 A (Note 4)	$T_j = 25^{\circ}C$	_	2.25	_	
$V_{CEsat}$	Collector-emitter saturation voltage		T <sub>j</sub> = 125°C	_	2.90	_	V
		V <sub>GE</sub> = 15 V	T <sub>j</sub> = 150°C	_	3.00	3.50	
	Turn-on delay time		T <sub>j</sub> = 25°C	_	_	_	μs
t <sub>d(on)</sub>			T <sub>j</sub> = 125°C	_	0.55	_	
			T <sub>j</sub> = 150°C	_	0.55	1.00	
		V <sub>CC</sub> = 2800 V	T <sub>i</sub> = 25°C	_			
t <sub>r</sub>	Rise time	I <sub>C</sub> = 1350 A	T <sub>i</sub> = 125°C	_	0.25		μs
		$V_{GE} = \pm 15 \text{ V}$	T <sub>j</sub> = 150°C	_	0.25	0.25 0.50	
	Turn on switching operay (Note 5)	$R_{G(on)} = 2.4 \Omega$	T <sub>j</sub> = 25°C	_	5.85	_	
E <sub>on(10%)</sub>	Turn-on Switching energy	L <sub>s</sub> = 150 nH	T <sub>j</sub> = 125°C	_	6.25	_	J
, ,	per pulse	Inductive load	T <sub>j</sub> = 150°C	_	6.30		
	Turn on switching operay (Note 6)		T <sub>j</sub> = 25°C	_	5.95		J
E <sub>on</sub>	Turn-on switching energy		T <sub>i</sub> = 125°C	_	6.60		
	per pulse		T <sub>i</sub> = 150°C	_	6.65		
			T <sub>i</sub> = 25°C	_			
$t_{d(off)}$	Turn-off delay time		T <sub>i</sub> = 125°C	_	7.00		μs
, ,			T <sub>i</sub> = 150°C	_	7.20	10.0	
		V <sub>CC</sub> = 2800 V	T <sub>i</sub> = 25°C	_	_	_	
$t_{f}$	Fall time	I <sub>C</sub> = 1350 A	T <sub>i</sub> = 125°C	_	0.50	_	μs
1		$V_{GE} = \pm 15 \text{ V}$	T <sub>i</sub> = 150°C	_	0.50	1.20	╡ ' │
	Turn off switching operay (Note 5)	$R_{G(off)} = 30 \Omega$	T <sub>i</sub> = 25°C	_	3.90	_	
E <sub>off(10%)</sub>	runr-on switching energy	L <sub>s</sub> = 150 nH	T <sub>j</sub> = 125°C	_	5.30		J
. ,	per pulse	Inductive load	T <sub>j</sub> = 150°C	_	5.60		
	T (Aloto G)		T <sub>j</sub> = 25°C	_	4.35	_	
$E_{off}$	Turn-off switching energy (Note 6) per pulse	witching energy (Note 6)	T <sub>i</sub> = 125°C	_	5.95	_	J
L-off			T <sub>i</sub> = 150°C	_	6.25	_	

#### < High Voltage Insulated Gate Bipolar Transistor: HVIGBT >

### CM1350HG-90X

HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

#### **ELECTRICAL CHARACTERISTICS (continuation)**

Cumbal	Item		Conditions		Limits			Unit	
Symbol	item		Conditions		Min	Тур	Max	Unit	
			1 4250 A (Note 4)	T <sub>j</sub> = 25°C	_	2.35	_		
$V_{EC}$	Emitter-collector voltage	(Note 2)	$I_E = 1350 \text{ A}^{\text{(Note 4)}}$	T <sub>j</sub> = 125°C	_	2.90	_	V	
			$V_{GE} = 0 V$	T <sub>j</sub> = 150°C	_	3.00	3.50	1	
				$T_j = 25^{\circ}C$	_	_	_		
t <sub>rr</sub>	Reverse recovery time	(Note 2)		$T_j = 125$ °C	_	1.45	_	μs	
				$T_j = 150$ °C	_	1.70	_		
				$T_j = 25^{\circ}C$	_	_	_		
Irr	Reverse recovery current	(Note 2)		$T_j = 125$ °C	_	1900	_	Α	
			$T_j = 150$ °C	_	1900	_			
			V <sub>CC</sub> = 2800 V	$T_j = 25^{\circ}C$	_	_			
Q <sub>rr(10%)</sub>	Reverse recovery charge	(Note 2,7)	I <sub>C</sub> = 1350 A	$T_{j} = 125^{\circ}C$	_	2450		μC	
		$V_{GE} = \pm 15 \text{ V}$	$T_j = 150$ °C	_	2500				
			$R_{G(on)} = 2.4 \Omega$	$T_j = 25^{\circ}C$	_	_			
$Q_{rr}$	Reverse recovery charge	(Note 2,6)	$L_{s} = 150 \text{ nH}$	$T_j = 125$ °C	_	2560		μC	
			Inductive load	$T_j = 150$ °C	_	2600	_		
	Poverse recovery energy	(Note 2,5)		$T_j = 25^{\circ}C$	_	3.05			
E <sub>rec(10%)</sub>	Reverse recovery energy per pulse	, =,0)		$T_j = 125$ °C	_	3.90	_	J	
	hei hnise			$T_j = 150$ °C	_	4.00			
	(Note 2.6)	(Note 2,6)		$T_j = 25^{\circ}C$	_	3.10	_		
E <sub>rec</sub>	Reverse recovery energy	, · · · · · · · · · · · · · · · · · · ·		T <sub>j</sub> = 125°C	_	4.20		J	
	per pulse			$T_j = 150$ °C	_	4.30	_		

#### THERMAL CHARACTERISTICS

Symbol	ltom	Conditions	Limits			Unit
Symbol	item	Item Conditions		Тур	Max	Offic
R <sub>th(j-c)Q</sub>	Thermal resistance	Junction to Case, IGBT part			8.5	K/kW
$R_{th(j-c)D}$		Junction to Case, FWDi part			13.0	K/kW
R <sub>th(c-s)</sub>	Contact thermal resistance	Case to heat sink $\lambda_{grease} = 1W/m \cdot k$ , $D_{(c-s)} = 80 \mu m$		5.0		K/kW

#### **MECHANICAL CHARACTERISTICS**

Symbol	Item	Conditions	Limits			l lat
			Min	Тур	Max	Unit
M <sub>t</sub>		M8 : Main terminals screw	7.0	1	19.0	N-m
Ms	Mounting torque	M6: Mounting screw	3.0	1	6.0	N⋅m
M <sub>t</sub>		M4 : Auxiliary terminals screw	1.0		3.0	N⋅m
m	Mass			1.5	_	kg
CTI	Comparative tracking index		600	_	_	_
d <sub>a</sub>	Clearance		26.0	1	1	mm
d <sub>s</sub>	Creepage distance		56.0	_	_	mm
L <sub>P CE</sub>	Parasitic stray inductance			13.5	_	nΗ
R <sub>CC'+EE'</sub>	Internal lead resistance	T <sub>C</sub> = 25 °C		0.12	1	mΩ

 $Note 1. \qquad \text{Pulse width and repetition rate should be such that junction temperature } (T_j) \ does \ not \ exceed \ T_{jopmax} \ rating.$ 

Note2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD<sub>i</sub>).

Note3. Junction temperature ( $T_j$ ) should not exceed  $T_{jmax}$  rating (150°C).

Note4. Pulse width and repetition rate should be such as to cause negligible temperature rise.

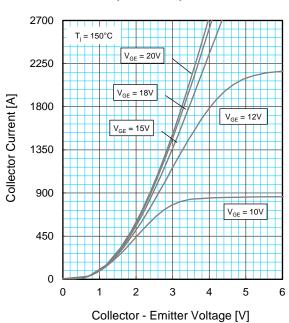
Note5. The integration range of switching energies is from  $10\%V_{CE}$  to  $10\%I_{C}(10\%I_{E})$ .

Note6. Definition of all items is according to IEC 60747, unless otherwise specified.

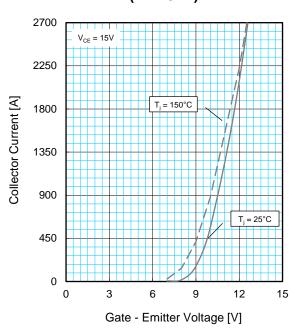
Note7. The integration range of reverse recovery charge is from  $I_E$  = 0A to 10% $I_E$ .

<del>.</del>

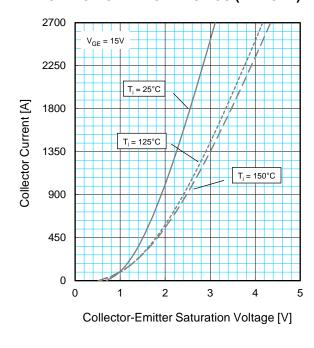
# OUTPUT CHARACTERISTICS (TYPICAL)



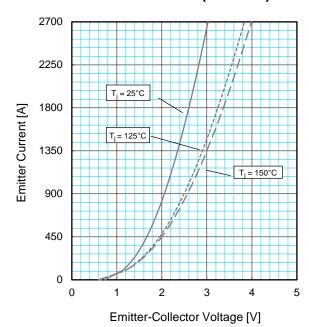
# TRANSFER CHARACTERISTICS (TYPICAL)



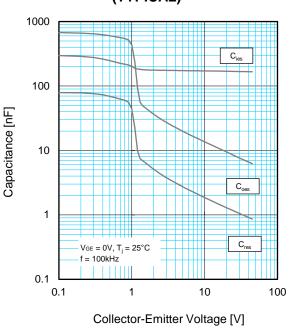
## COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



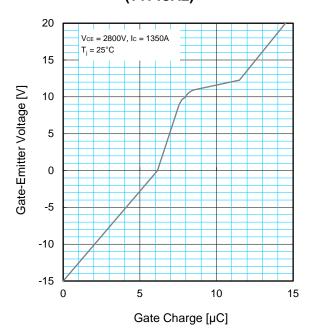
# FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



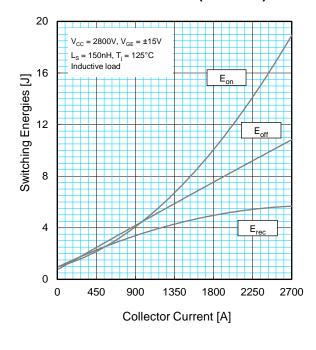
# CAPACITANCE CHARACTERISTICS (TYPICAL)



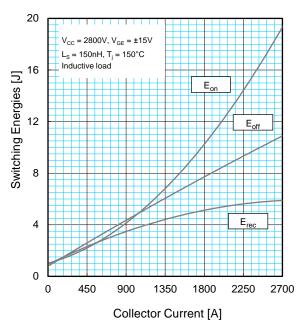
# GATE CHARGE CHARACTERISTICS (TYPICAL)



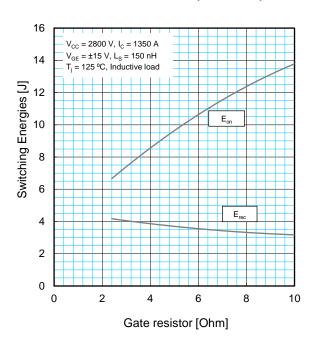
# HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



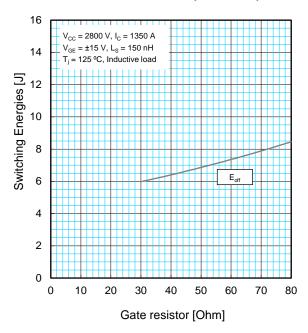
# HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



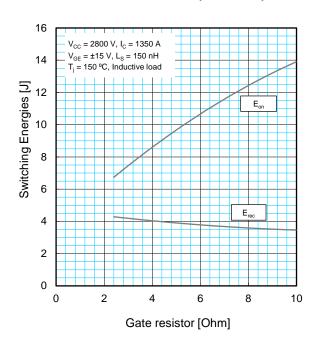
# HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



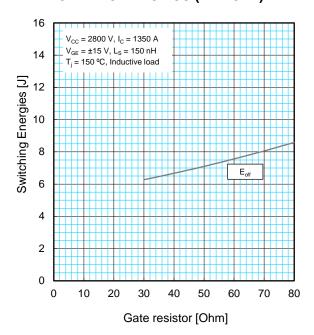
# HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



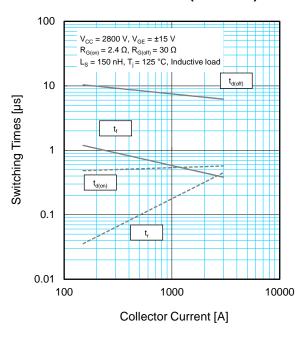
# HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



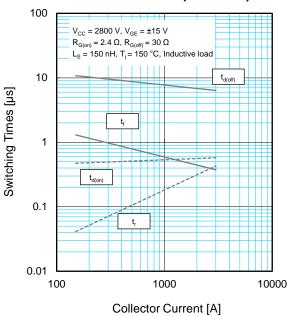
# HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



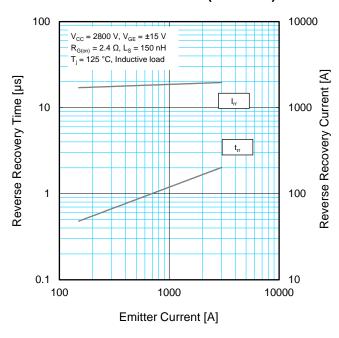
# HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



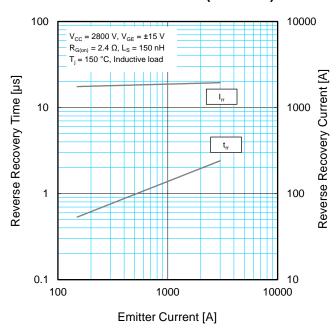
# HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



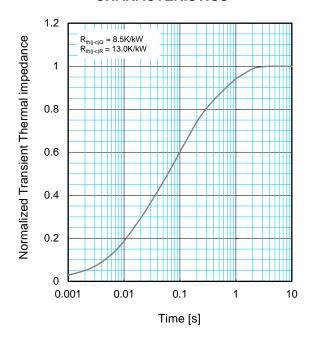
# FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



# FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



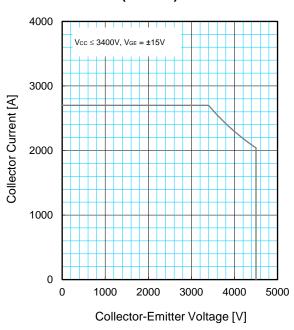
## TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



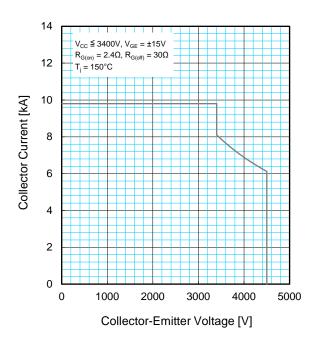
$$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_{i} \left\{ 1 - \exp^{\left(-\frac{t}{\tau_{i}}\right)} \right\}$$

	1	2	3	4
R <sub>i</sub> / R <sub>th(j-c)</sub> :	0.0096	0.1893	0.4044	0.3967
τ <sub>i</sub> [sec]:	0.0001	0.0058	0.0602	0.3512

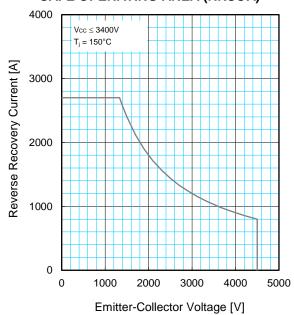
# REVERSE BIAS SAFE OPERATING AREA (RBSOA)



# SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)



# FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)



5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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